

Charles "Larry" Church

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Objective:

Apply my experience in hardware and software development and test/QA in a leading edge company to produce and deliver products - on schedule and on budget.

Experience:

2/11 to Present Cetus Consulting Services, Sunol, CA

Sr. Consulting Engineer - Contract consulting. Web site and email development for small business. Developed a Java/MySQL based inventory system that automated available inventory on web site.

10/04 to 2/11 ebrary, Inc., Palo Alto, CA

Sr. Systems Engineer - QA software releases of the ebrary Reader on Windows, Macintosh, and Linux. Regressed bugs, updated test cases, and tested per-release Web pages. Performed system tasks on the ebrary server/Oracle database. Wrote system test programs using Java and SQL

1/04 to 9/04 Cetus Consulting Services, Fremont, CA

Sr. Consulting Engineer - General consulting to business on Windows and Unix/Linux networking, IT, security, software development and software quality and testing.

8/03 to 1/04 Montague Solutions, San Jose, CA

Software QA & Test - Monta Vista Software, Sunnyvale, CA

QA Engineer in the Product Development group. Developed new and modified existing tests, test harness and testing infrastructure. Installed, tested and trouble shot Monta Vista Linux for embedded application and device developers, on some 30 development boards with 6 different processor architectures.

7/02 to 8/03 Bay Area Network Consulting Services, San Ramon, CA

Sr. Consulting Engineer - Consulting on software development, software quality, Unix and Linux systems issues, email and networking. Consulted to small business about networking, network security, email and internet connectivity.

8/98 to 7/02 PLX Technology, Inc., Sunnyvale, CA

Sr. Field Applications Engineer - Responsible for southern California, Arizona, New Mexico, Colorado, Texas, Southern Nevada, Utah, and Wyoming. Was the primary technical contact for all customer issues. Gave technical presentations to engineers and management. Wrote application notes, design proposals, and specifications. Was the manager of the Technical Applications (world wide technical support) department for one year. Ensured that the chip/software errata list was accurate and delivered to customers in timely manor. Instituted a "Bug Review Committee" to ensure that chip errata and software bugs were fixed in subsequent releases.

5/96 to 8/98 Adobe Systems, Inc., San Jose, CA

QA Engineer - Unix Acrobat QA. Responsible for software test of Unix Acrobat 3.0 Reader and Exchange products on Solaris/SunOS, HP-UX, AIX, Irix, and Linux systems and assisted with the Windows product testing. Did system administration on Unix/Linux systems.

2/95 to 3/96 nCUBE Corp., Foster City, CA

Sr. Test Engineer - Product Integration, Evaluation, & Release Group. Responsible for software test of SCSI and ATM interfaces on nCUBE massively parallel computer systems. Wrote test plans and developed I/O tests and some system calls and some system utilities for nCUBE's new generation system under a custom micro-kernel operating system and under a port of Plan 9.

2/94 to 2/95 Kubota Pacific Computer, Inc., Santa Clara, CA

QA/Test Engineer - Release and Software Quality Assurance group. Was responsible for writing and executing the QA test plan for various 3-D graphics APIs, and for an ISV application. Wrote C, perl, and shell scripts to test high performance 3-D graphics accelerator. Also wrote tests for an automated video test system.

2/93 to 2/94 Sunday & Associates, Inc., Wayzata, MN

Software QA & Test - Frame Corp. (11/93 to 2/94)

QA/Test Engineer in the X11 group. Tested FrameMaker4 on Motorola 88k Unix. Executed existing test procedures and generated new procedures to test new functionality.

Software QA & Test - IBM Corp. (2/93 to 9/93)

QA/Test Engineer in the Test and Customer Support group in the multimedia group of the Personal Software division in Mt. View, CA. Developed and tested the network portion of an AIX based multimedia file server. The tests were C, C++, and Borne shell programs. Tested and configured token ring and ethernet networks containing AIX hosts and PC clients.

2/84 to 2/93 Digital Equipment Corporation, Palo Alto, CA

Software QA - Western Software Laboratory (6/86 to 2/93)

QA Project Leader for a multimedia server project. Coordinated all QA activities including test development and methods. Focal point and QA interface to engineering, project and support organizations.

QA Project Leader for ULTRIX 4.2A Workstation Software Was the QA interface at the project level, including being part of the ship/no ship decision making process. Wrote the project level QA plan. Ensured that needed bug fixes were included in builds.

Software QA Engineer responsible for lab hardware and software. Responsible for maintaining the accuracy of the bug report database. Established and chaired the Bug Report Review Committee to review, track, and resolve problems reported. Responsible for the quality portions of Customer Service and Support Engineering's Service Requirements Document for seven Digital Unix releases. Tested installations and verified bug reports and fixes for various components. Principal interface between the hardware design group for hardware and software field test and software releases.

Principal Software Engineer involved with the MIT X v11 server and Xlib. Ported a terminal emulator (which later became a product) to X11. Was the UNIX consultant to the Terminal Business Unit for a terminal emulator (wrote Unix specific code for DECterm).

Manager of Power and Packaging - Titan Project (11/84 to 6/86).

In Corporate Research and Architecture group, responsible for managing the mechanical design and the power system design of an high speed RISC computer. Was also responsible for the solution of signal integrity issues.

Principal Engineer - Trilogy Technology Development Project (2/84 to 11/84)

Responsible for interconnect system design for a high technology based very high performance VAX; including connectors and interconnect cable systems. Responsible for wiring rules and delay predictions.

11/82 to 2/84 STC/Computer Research Corporation, Santa Clara, CA

Staff Engineer - CMOS Circuit Design Department

Responsible for all logic designer support (wiring rules, delay equations, and simulation). Investigated system interconnect problems. Primary interface to Design Automation Group. Wrote design manuals and device specifications. Responsible for the evaluation of custom CMOS static RAMs.

5/73 to 10/82 Amdahl Corporation, Sunnyvale, CA

System Design Engineer - Test Equipment Software Department (2/82 to 10/82) Software design to control a network analyzer and an LCR bridge in an automated semiconductor test system.

System Design Engineer - Circuit Development and Packaging Department (6/79 to 2/82) Designed and evaluated PWBs and interconnects used in Amdahl systems. Developed performance models and wrote and maintained design manuals. Involved in system clock design and established clock tuning procedures. Interface to Design Automation group. Key participant in selecting vendors for gate arrays.

Senior Technician - Component Engineering & Circuit Development Department (11/74 to 6/79) Evaluated PWB's and chip problems. Completed studies for and wrote portions of several design manuals. Managed lab and equipment use by various departments. Developed and wrote software for a component tester. Developed circuits and techniques for use in custom high speed memory tester.

Technician - Component Engineering (5/73 to 11/74)

Evaluated ECL logic and RAMs, TTL and linear integrated circuits and other devices.

2/72 to 5/73 Signetics Corporation, Sunnyvale, CA

Technician - Evaluated ECL 10K devices. Designed, built and debugged breadboards, test fixtures, and testers.

EDUCATION

Pennsylvania State University, McKeesport, PA

Central Westmoreland Technical School, Youngwood, PA

REFERENCES

Professional references are available on request.